



Keysight EEs of EDA “How to” Video



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The Keysight EEs of EDA “How to” video app-note collection addresses many challenging technical topics in RF & microwave, signal integrity, device modeling, and signal-processing design. Each video has an associated workspace that you can download to help get you started.

Find the entire “How to” video collection here: <http://www.keysight.com/find/eesof-how-to-videos>

Topics include:

- [How to Design an RF Power Amplifier: Class E](#)
- [How to Use Fixture De-embedding to Match SI Simulations to Measurements](#)
- [How to Design an RF Power Amplifier: The Basics](#)
- [How to Design an RF Power Amplifier: Class A, AB, and B](#)
- [How to Design an RF Power Amplifier: Class F](#)
- [How to Use Envelope Tracking to Improve Power Amplifier Efficiency](#)
- [How to Predict Package and Bondwire Effects on Your RF Module Designs](#)
- [How to Optimize the Performance of Your RF Layout](#)
- [How to Characterize and Optimize Connector to Board Transition Designs](#)
- [How to Design RF and Microwave Impedance Matching Networks](#)
- [How to Apply EM Port Calibration to Improve Simulation Accuracy \(MoM\)](#)
- [How to Use “Design of Experiments” to Create Robust Designs with High Yield](#)
- [How to Setup and Run Load Pull Simulations: The Basics](#)
- [How to Extract SRAM Models](#)
- [How to Make Accurate, Automated RF Wafer-level Measurements](#)

